

FIG. 2

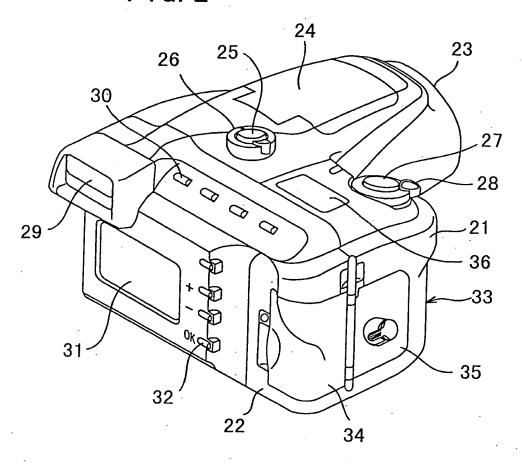


FIG. 3 6 6-4 6-5 6-1 6_7 6-3 SYNTHES I Z I NG CKT COMPRESS I ON CKT SE MEMORY SELECTOR LE MEMORY 6-2 6-6 SIGNAL PROCESSING CKT -8 8A^ CPU

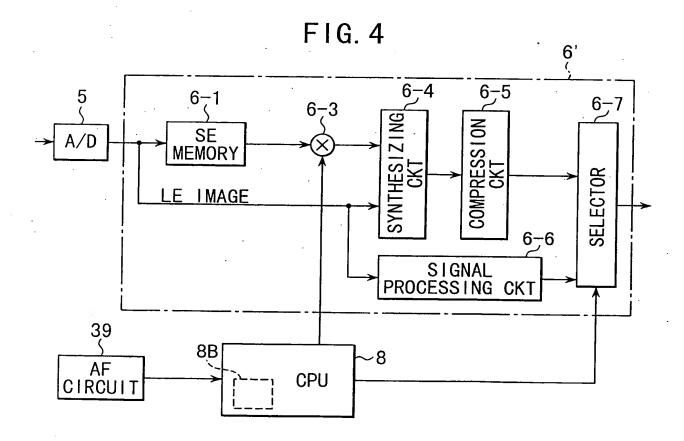


FIG. 5A

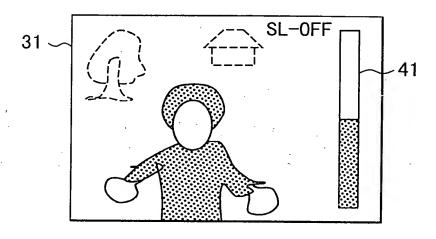
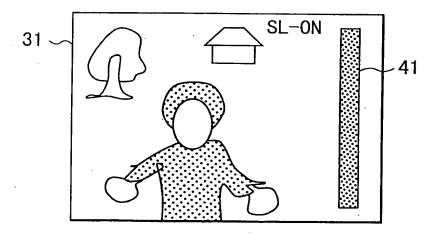
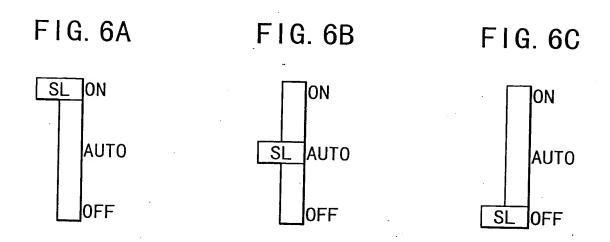
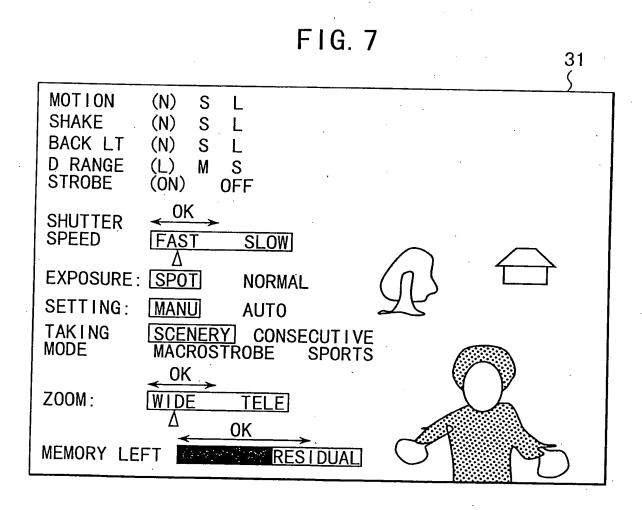
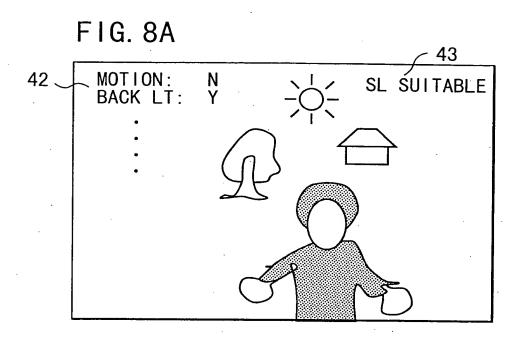


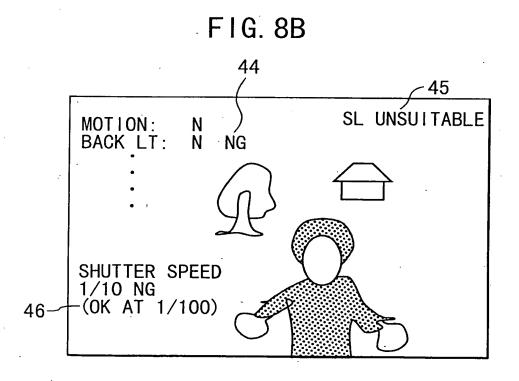
FIG. 5B











F1G. 9

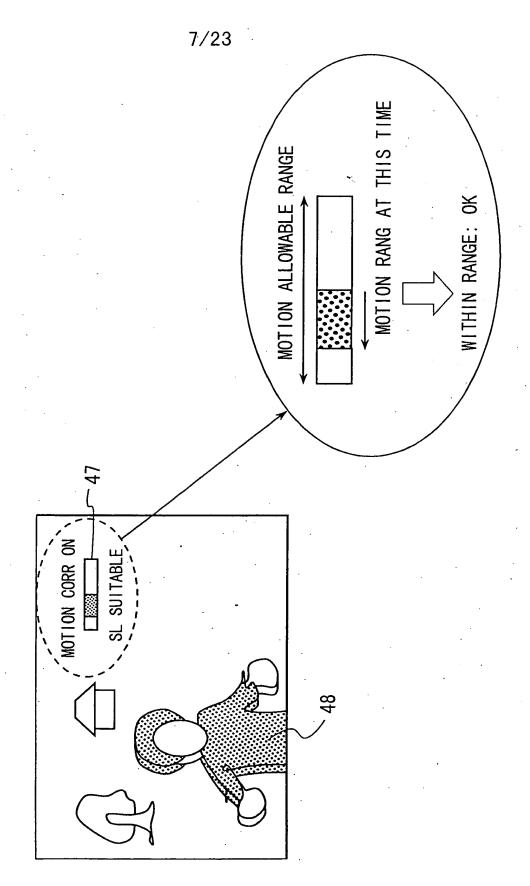


FIG. 10A

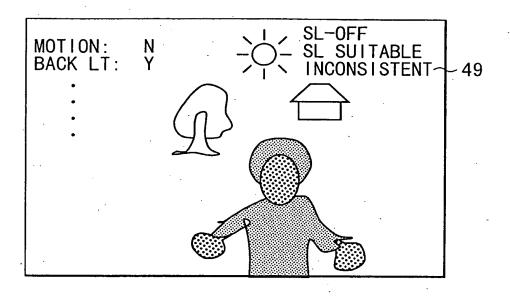


FIG. 10B

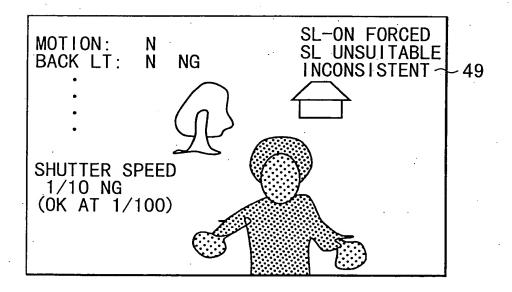


FIG. 11A

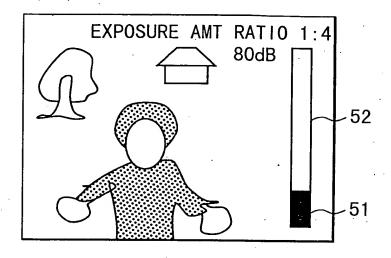


FIG. 11B

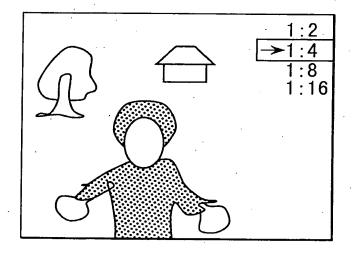


FIG. 12A

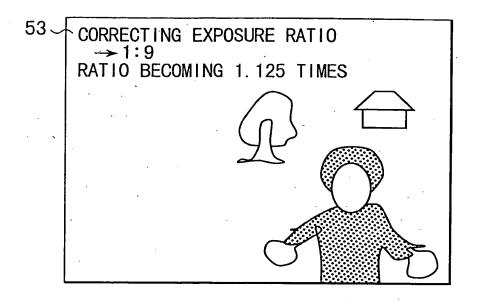


FIG. 12B

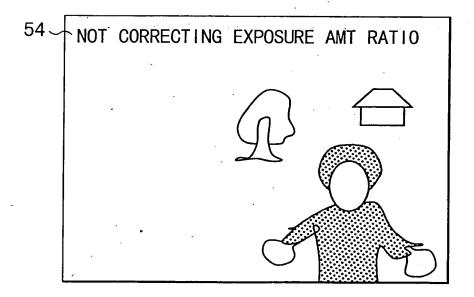


FIG. 13A

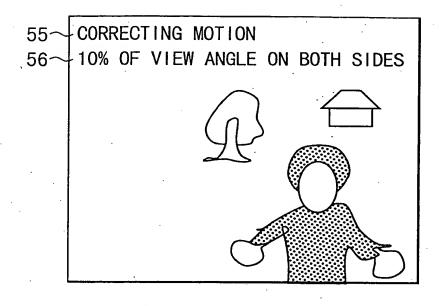


FIG. 13B

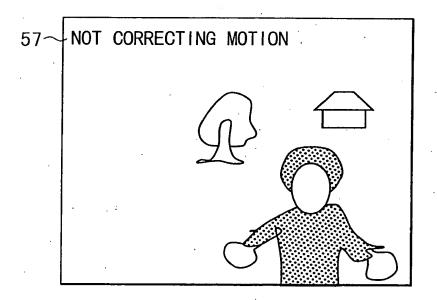


FIG. 14

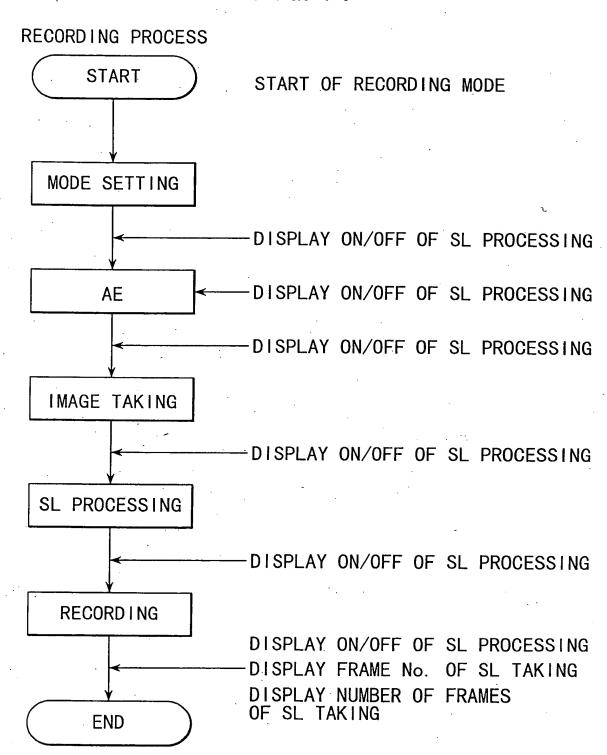


FIG. 15A

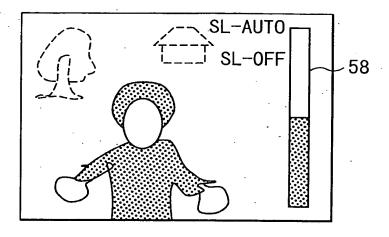


FIG. 15B

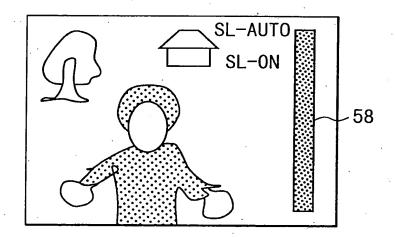


FIG. 16

1 : SL 2 : SL 3 : NOR 4 : SL		

FIG. 17

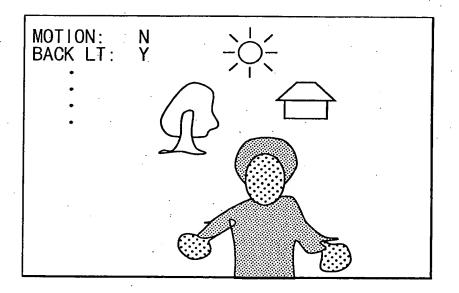


FIG. 18A

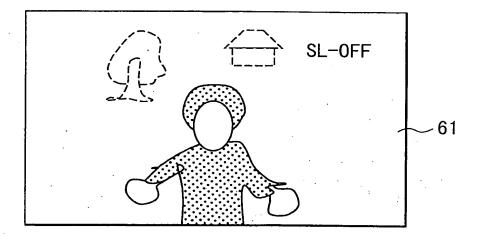


FIG. 18B

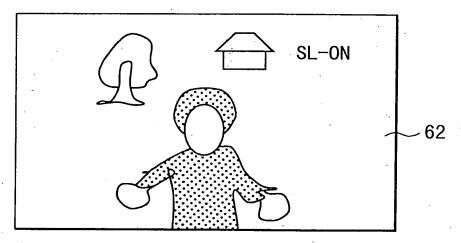


FIG. 19

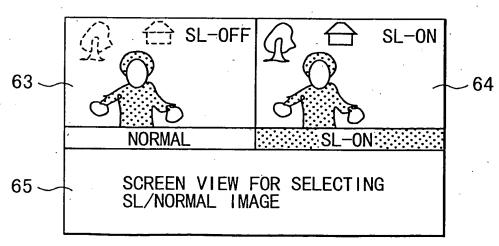


FIG. 20A

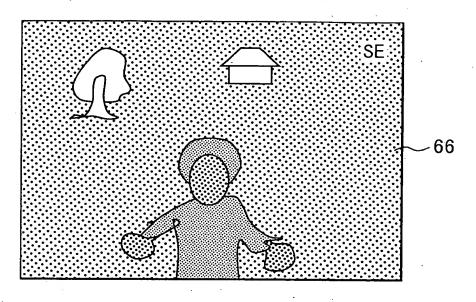


FIG. 20B

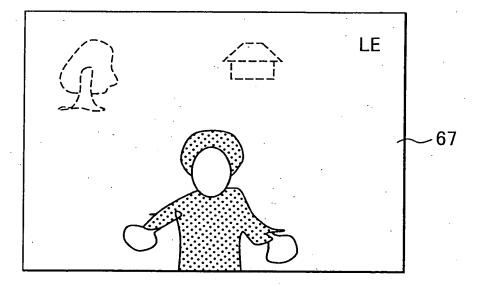


FIG. 21A

71~	SETTING CHANGE MODE CHANGING CAMERA SETTING Item 1 ORIG VALUE NEW VALUE Item 2 Item 3
	•

FIG. 21B

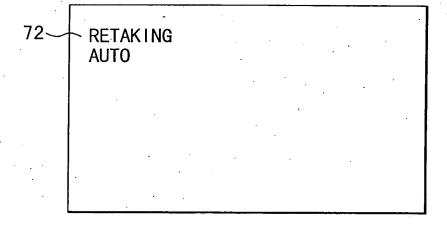


FIG. 22A

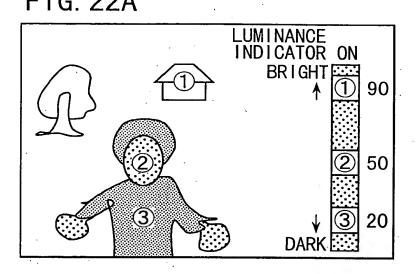


FIG. 22B

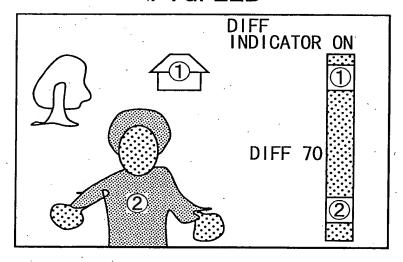


FIG. 22C

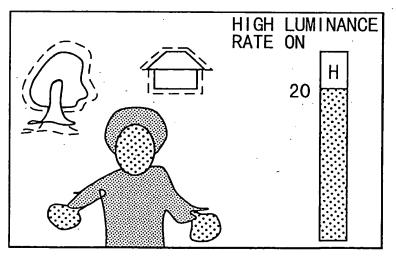


FIG. 23A

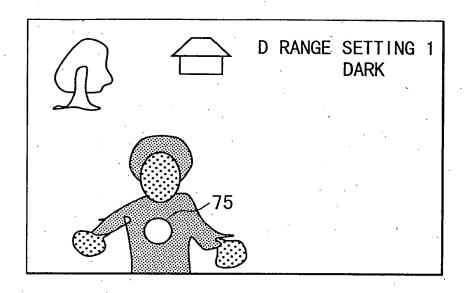


FIG. 23B

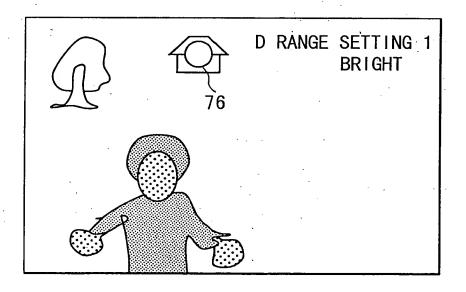


FIG. 24A

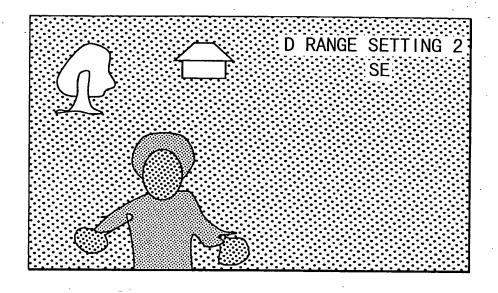


FIG. 24B

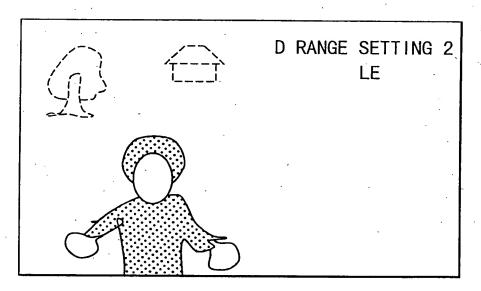


FIG. 25

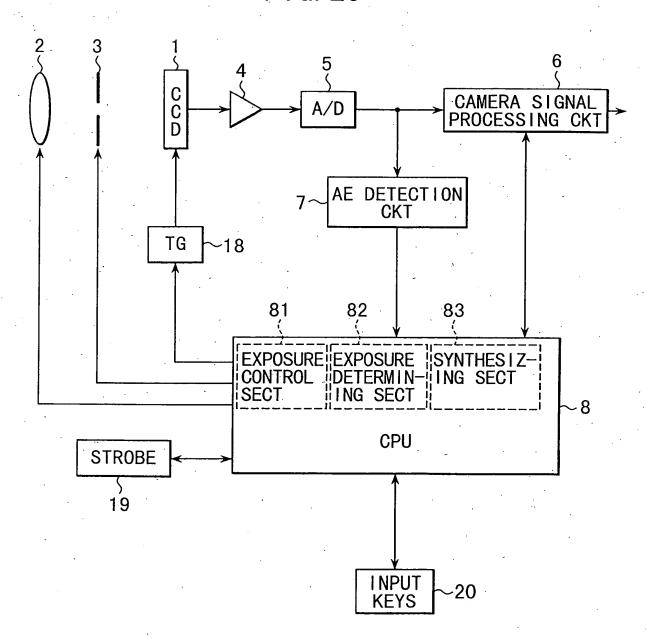


FIG. 26

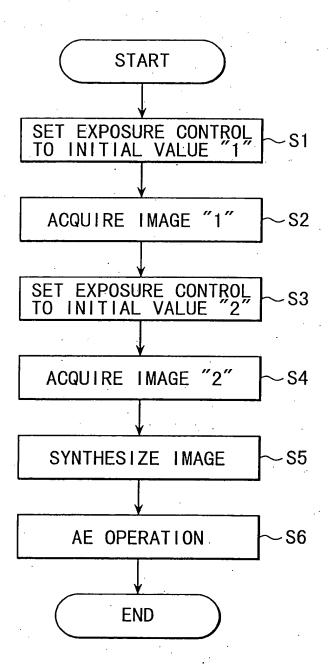


FIG. 27A

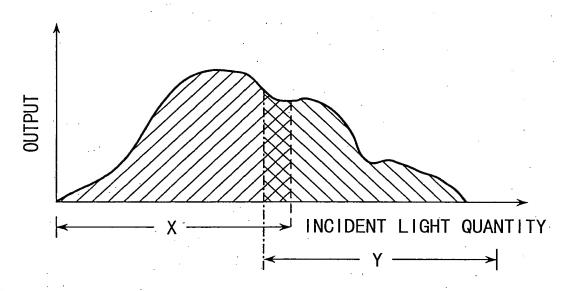


FIG. 27B

